# **Sensor Platform**

# PSU Capstone Project

# WPR

Last Week: 4/11-4/25

Next Week: 4/25 – 5/2

# Steve Peirce

## Last week:

* Forgot to submit WPR as was swamped with working on project
* Met with team and Dr. Zurk to discuss next steps, test plan, etc.
* Populated 2 boards
* Tested new board functionality
* Debugged new board
* Attempted to connect to new board
* Discovered incorrect IC in place of V switch, desoldered, replaced
* Discovered issue with layout on V switch IC, correct footprint, wrong connections
* Connected replacement IC with “Mod-wire”
* Discovered main STM32F205 was rotated 180 degrees, desoldered, replaced
* Met with Golriz about next board revision, USB to UART IC selection, etc.

## Next week:

* Continue debugging hardware
* Meet with Andrew and Golriz about board rev 2
* Port existing code to new board
* Implement working SDIO
* Return to remainder of firmware programming for new board
* Disperse software/firmware testing jobs
* Begin poster layout

## Issues:

* Board not yet functional
* New board layout but be sent to fab immediately
* Incorrect IC placement/layout
* Processor was rotated 180 degrees
* SDIO not fully functional
* Serial comm protocol not fully functional
* Team schedule not fully revised/submitted
* Decision Matrices not submitted
* ….Time is running out!